Q	.P. 0	Code: 2	0EC04	11										ł	$\mathbf{R}$	
F	leg.	No:					T					Τ	]			
		SIDD	HART	HIN	STITI	TE C	DE EN	IGINE	ERIN	IG &	TEC	HNOI	_ .OG¥	(:: PU	TTUR	
					SIII		(Al	UTON	OMOL	JS)	ILC.		1001			
		1	B. Tec	h II )	ear ll	Seme	ester	Regu	lar E	xami	natio	ns Od	ctobe	r-202	2	
					LINE	EAR &	k DIO	GITAL	IC A	PPLI	CAT	IONS				
					(Elec	tronics	s and	Comm	unicat	ion E	ngine	ering)				
Т	ime:	3 hours												М	lax. Mar	ks: 60
					(An	swer a	all Fix	<i>ie</i> Unit	s 5 x 1	2 = 0	60 Ma	rks)				
					(2 11	Swer c			TI		0 101u	.r.k5)				
1	9	Describe	- about	the h	lock di	iaoram	ofO	n-Amr							L.1	8M
1	h	Determi	ne the	outpu	it volta	ge of a	a diff	erentia	). I Amp	lifier	for th	e inpu	t volta	ages o	f L3	4M
	$300\mu V \& 240\mu V$ . The Differential gain of the amplifier is 5000.the value of the CMRR is 100												2	TIVA		
								0	R							
2	a l	Draw and Explain about the Non-Inverting Amplifier.									L2	<b>6M</b>				
	b 1	Discuss	about	Schm	itt trigg	ger wit	h nea	t sketc	hes. <b>T-II</b>						L3	6 <b>M</b>
3	a l	Explain	about	the op	eration	n of W	ien B	ridge (	Dscilla	tor us	ing O	p-Amp	0		L3	<b>6M</b>
	b l f	<b>b</b> Draw the circuit of a 1 <sup>st</sup> order low pass Butterworth filter and discuss its transfer functions.											r <b>L2</b>	6M		
								0	R							
4	al	Discuss	about .	All pa	ss filte	r with	neat	sketch.		1	1	EEE L			L2	8M
	bl	JISCUSS	about	the Di	scharg	e and o	contro		ige pin	role	in the	555 tii	mer.		L2	41/1
5	a l	Draw an	id Expl	lain al	oout the	e blocl	k sch	ematics	s of PL	L.					L2	<b>8M</b>
	bl	Define I	'LL an	d List	the ap	plicati	ons c	of PLL.							L3	4M
	Due			- 1	+ D 2D	DAG	:41	0	R						Т 1	1334
0	Dra	w and e	explain	abou	t K-2K	DAC	with	an exa	mple. Γ-IV						LI	1211
7	a l	Explain	about	functi	ons and	d proc	edure	s with	an exa	imple					L2	6M
	b	Write a	VHDL	entit	y and A	Archite	ectur	e for th	ne follo	owing	func	tion. F	$f(\mathbf{x}) =$	(a + b	) L2	6M
	(	(c+d) A	lso dra	aw the	e releva	ant log	,1C d18	igram.	n							
Q	Dec	ion the	logic	irouit	and w	rito VI	UDI	nrogro	<b>K</b> m for	the fo	llowi	na fun	otion	$\mathbf{F}(\mathbf{V})$	- 12	121
0	$\Sigma A$	, B, C,	D(0, 2)	2, 5, 7	, 8, 10,	13, 15	(5) + d	(1, 6,	11). <b>T V</b>	ule lo	nown	ing run	ction.	$\Gamma(\Lambda)$	- L3	12111
9	a l	Explain	the op	eratio	on of st	andard	dIC	for 3X	8 deco	der v	vith no	ecessa	ry tru	th tabl	e L2	6M
	6 b	ina inte	rnal ar	cnitec	ture.	h	Da	adar							TO	(N/
	IJ	write a	VIDL	code	ior the	: a00V	e Dec	ouer.	R						L2	OIVI
10	a 1	Distino	ish he	tween	latch	and fli	n flor	h Shou	n v the L	noie e	liaora	m for <sup>1</sup>	hoth	Exnlai	n I.4	6M
	1	he oper	ation v	vith th	ne heln	of fin	iction	table	, the h	05101	ingia	101		Supra	1.4	VIVL
	b	Write a	VHDL	code	for a [	D-flip f	flop i	n behav	vioral	mode	l.				L2	6 <b>M</b>
								*** FN	JD ***	ĸ						

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